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Terms	Documents
(configur\$5 near5 ROM) same (serial adj1 bus)	38

Database:

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Search:

L2

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Monday, June 21, 2004 [Printable Copy](#) [Create Case](#)

Set Name **Query**
side by side

Hit Count **Set Name**
result set

DB=USPT,USOC; PLUR=YES; OP=OR

<u>L2</u> (configur\$5 near5 ROM) same (serial adj1 bus)	38	<u>L2</u>
<u>L1</u> (configur\$5 near5 ROM) same (serial adj1 bus) same layer	7	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2	0

Database: US Pre-Grant Publication Full-Text Database
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Search: L3

Search History

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Set Name Query

side by side

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L3 L2

0 L3

DB=USPT,USOC; PLUR=YES; OP=OR

L2 (configur\$5 near5 ROM) same (serial adj1 bus)

38 L2

L1 (configur\$5 near5 ROM) same (serial adj1 bus) same layer

7 L1

Hit Count Set Name

result set

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(358/1.15 370/463 709/253 709/301 709/302 709/220 710/104 710/105 710/106 710/62 710/63 710/2 710/305 710/8 714/1).ccls.	6337

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Search:

L4

Refine Search

Recall Text Clear Interrupt

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Set	Name	Query	Hit Count	Set Name
side by side				result set
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L4		710/104,105,106,62,63,2,305,8;709/253,301,302,220;370/463;714/1;358/1.15.ccls.	6337	L4
		DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L3	L2		0	L3
		DB=USPT,USOC; PLUR=YES; OP=OR		
L2		(configur\$5 near5 ROM) same (serial adj1 bus)	38	L2
L1		(configur\$5 near5 ROM) same (serial adj1 bus) same layer	7	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2 and L4	21

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Database:	L5	Refine Search
Search:	<input type="text" value="L5"/> <input type="button" value="Recall Text"/> <input type="button" value="Clear"/> <input type="button" value="Interrupt"/>	

Search History

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<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit</u>	<u>Set</u>
<u>Count</u>			<u>result</u>	<u>set</u>
side by side				
		DB=USPT,USOC; PLUR=YES; OP=OR		
L5	L2 and L4		21	L5
L4	710/104,105,106,62,63,2,305,8;709/253,301,302,220;370/463;714/1;358/1.15.ccls.		6337	L4
	DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L3	L2		0	L3
	DB=USPT,USOC; PLUR=YES; OP=OR			
L2	(configur\$5 near5 ROM) same (serial adj1 bus)		38	L2
L1	(configur\$5 near5 ROM) same (serial adj1 bus) same layer		7	L1

END OF SEARCH HISTORY

EAST - [Untitled1:1]

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Drafts Pending Active L1: (1) (configur\$5 near5 ROM) Failed Saved Favorites Tagged (0) UDC Queue Trash

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DBs USPAT Default operator: OR Plurals Highlight all hit terms initially

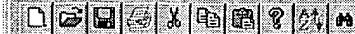
BRS I... ISR Image Text HTML

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1 BRS	L1	1	(configur\$5 near5 ROM) same (serial adj1 bus)	USPAT	2004/06/21 08:49			0

Start

EAST - [Untitled1:1]

File View Edit Tools Window Help



- Drafts
- Pending
- Active
- L1: (1) (configur\$5 near5 ROM)
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search

DBs: USPAT Plurals Highlight all hit terms initially

Default operator: OR

(configur\$5 near5 ROM) same (serial adj1 bus) same (each or individual)

U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	US 6665020 B1	20031216	14	Digital television apparatus for controlling a peripheral	348/552	710/107; 725/80	

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configur* and rom and (link or node)

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Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Three-phase four wire voltage controlled AC line conditioner with unity input power factor and minimised output voltage harmonics***Lo, Y.-K.; Chen, C.-L.;*

Electric Power Applications, IEE Proceedings-, , Volume: 142 , Issue: 1 , Jan. : Pages:43 - 49

[\[Abstract\]](#) [\[PDF Full-Text \(428 KB\)\]](#) **IEE JNL****2 Development of a portable multi-functional patient monitor***Wong, C.; Chan, K.L.;*Engineering in Medicine and Biology Society, 2000. Proceedings of the 22nd / International Conference of the IEEE , Volume: 4 , 23-28 July 2000
Pages:2611 - 2614 vol.4[\[Abstract\]](#) [\[PDF Full-Text \(544 KB\)\]](#) **IEEE CNF****3 Requirements, configuration management and traceability for safe critical software***Romanski, G.;*Requirements Engineering Conference, 2003. Proceedings. 11th IEEE International , 8-12 Sept. 2003
Pages:304[\[Abstract\]](#) [\[PDF Full-Text \(175 KB\)\]](#) **IEEE CNF****4 A parallel ultra-high resolution MPEG-2 video decoder for PC cluster based tiled display systems***Han Chen; Kai Li; Bin Wei;*

Parallel and Distributed Processing Symposium., Proceedings International, IF 2002, Abstracts and CD-ROM , 15-19 April 2002



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**Requirements, configuration management and traceability for safety critical software**

Romanski, G.

Verocel, Inc.

This paper appears in: Requirements Engineering Conference, 2003. Proc 11th IEEE International

Publication Date: 8-12 Sept. 2003

On page(s): 304

ISSN: 1090-705X

Number of Pages: xv+378

Inspec Accession Number: 7914023

Abstract:

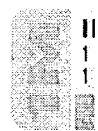
Software requirements are the focal point from which traceability to all related requirements are established during the certification of safety critical software. For the certification of air-borne software, the guidance document DO-178B, requires that the link between requirements, design, code and tests be documented and verified. The DO-178B document does not describe how this should be done, but it permits the reuse of information that is missing, to support the certification of commercial-off-the-shelf (COTS) products. Requirements were entered in a database and evolved through a sequence that enforced the states described in company process documents. Descriptions, source code, tests, results and so on were maintained in a configuration management (CM) system. The final audit was successful and the CD-ROM containing the requirements based certification package was accepted and commended. The approach and the lessons learned were presented.

Index Terms:

formal specification formal verification military computing safety-critical software software systems analysis CM system COTS product air-borne software certification package commercial-off-the-shelf company process document configuration management in reengineering safety critical software software requirement

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1. Document ID: US 6745256 B2

Using default format because multiple data bases are involved.

L1: Entry 1 of 7

File: USPT

Jun 1, 2004

US-PAT-NO: 6745256

DOCUMENT-IDENTIFIER: US 6745256 B2

TITLE: Information processing apparatus and method

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Suzuki; Naohisa	Yokohama			JP
Nakamura; Atsushi	Kawasaki			JP
Kobayashi; Makoto	Yokohama			JP
Katano; Kiyoshi	Chiba			JP

US-CL-CURRENT: 710/18; 710/19, 710/8

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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2. Document ID: US 6665020 B1

L1: Entry 2 of 7

File: USPT

Dec 16, 2003

US-PAT-NO: 6665020

DOCUMENT-IDENTIFIER: US 6665020 B1

TITLE: Digital television apparatus for controlling a peripheral device via a digital bus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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3. Document ID: US 6477589 B1

L1: Entry 3 of 7

File: USPT

Nov 5, 2002

US-PAT-NO: 6477589

DOCUMENT-IDENTIFIER: US 6477589 B1

** See image for Certificate of Correction **

TITLE: Information processing apparatus and method

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Search](#) | [Text Search](#) | [Claims](#) | [KMC](#) | [Drawn](#)

4. Document ID: US 6367026 B1

L1: Entry 4 of 7

File: USPT

Apr 2, 2002

US-PAT-NO: 6367026

DOCUMENT-IDENTIFIER: US 6367026 B1

TITLE: Unbalanced clock tree for a digital interface between an IEEE 1394 serial bus system and a personal computer interface (PCI)

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Search](#) | [Text Search](#) | [Claims](#) | [KMC](#) | [Drawn](#)

5. Document ID: US 6185622 B1

L1: Entry 5 of 7

File: USPT

Feb 6, 2001

US-PAT-NO: 6185622

DOCUMENT-IDENTIFIER: US 6185622 B1

TITLE: Electronic apparatus, communication speed information collection method, communication method between electronic apparatus and recording medium

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Search](#) | [Text Search](#) | [Claims](#) | [KMC](#) | [Drawn](#)

6. Document ID: US 6160796 A

L1: Entry 6 of 7

File: USPT

Dec 12, 2000

US-PAT-NO: 6160796

DOCUMENT-IDENTIFIER: US 6160796 A

TITLE: Method and system for updating device identification and status information after a local bus reset within a home audio/video network

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7. Document ID: US 6038625 A

L1: Entry 7 of 7

File: USPT

Mar 14, 2000

US-PAT-NO: 6038625

DOCUMENT-IDENTIFIER: US 6038625 A

TITLE: Method and system for providing a device identification mechanism within a consumer audio/video network

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Abstract](#) [Detailed Description](#) [Claims](#) [KOMC](#) [Drawn D](#)

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(configur\$5 near5 ROM) same (serial adj1 bus) same layer

7

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File: USPT

Jun 1, 2004

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DOCUMENT-IDENTIFIER: US 6745256 B2

TITLE: Information processing apparatus and method

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Suzuki; Naohisa	Yokohama			JP
Nakamura; Atsushi	Kawasaki			JP
Kobayashi; Makoto	Yokohama			JP
Katano; Kiyoshi	Chiba			JP

US-CL-CURRENT: 710/18; 710/19, 710/8

Full	Title	Citation	Front	Review	Classification	Date	Reference	Subject	Author	Assignee	Claims	KWIC	Drawn D
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2. Document ID: US 6665020 B1

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File: USPT

Dec 16, 2003

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TITLE: Digital television apparatus for controlling a peripheral device via a digital bus

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3. Document ID: US 6477589 B1

L1: Entry 3 of 7

File: USPT

Nov 5, 2002

US-PAT-NO: 6477589

DOCUMENT-IDENTIFIER: US 6477589 B1

** See image for Certificate of Correction **

TITLE: Information processing apparatus and method

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4. Document ID: US 6367026 B1

L1: Entry 4 of 7

File: USPT

Apr 2, 2002

US-PAT-NO: 6367026

DOCUMENT-IDENTIFIER: US 6367026 B1

TITLE: Unbalanced clock tree for a digital interface between an IEEE 1394 serial bus system and a personal computer interface (PCI)

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5. Document ID: US 6185622 B1

L1: Entry 5 of 7

File: USPT

Feb 6, 2001

US-PAT-NO: 6185622

DOCUMENT-IDENTIFIER: US 6185622 B1

TITLE: Electronic apparatus, communication speed information collection method, communication method between electronic apparatus and recording medium

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6. Document ID: US 6160796 A

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File: USPT

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TITLE: Method and system for updating device identification and status information after a local bus reset within a home audio/video network

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7. Document ID: US 6038625 A

L1: Entry 7 of 7

File: USPT

Mar 14, 2000

US-PAT-NO: 6038625

DOCUMENT-IDENTIFIER: US 6038625 A

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TITLE: Method and system for providing a device identification mechanism within a consumer audio/video network

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Terms	Documents
(configur\$5 near5 ROM) same (serial adj1 bus) same layer	7

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L6: Entry 2 of 10

File: USPT

Dec 30, 2003

US-PAT-NO: 6671768

DOCUMENT-IDENTIFIER: US 6671768 B1

TITLE: System and method for providing dynamic configuration ROM using double image buffers for use with serial bus devices

DATE-ISSUED: December 30, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Brown; Steven W.	San Jose	CA		

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Apple Computer, Inc.	Cupertino	CA			02

APPL-NO: 09/ 431409 [PALM]

DATE FILED: November 1, 1999

INT-CL: [07] G06 F 12/00, G06 F 13/00

US-CL-ISSUED: 711/102; 711/5, 711/170, 713/1, 713/100

US-CL-CURRENT: 711/102; 711/170, 711/5, 713/1, 713/100

FIELD-OF-SEARCH: 711/170, 711/102, 711/103, 711/5, 711/162, 710/101, 710/103, 710/104, 717/11, 714/6, 714/7, 713/1, 713/2, 713/100

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
<input type="checkbox"/> <u>5568641</u>	October 1996	Nelson	713/2
<input type="checkbox"/> <u>5701492</u>	December 1997	Wadsworth et al.	717/11
<input type="checkbox"/> <u>5809331</u>	September 1998	Staats et al.	710/10
<input type="checkbox"/> <u>5835761</u>	November 1998	Ishii et al.	713/2
<input type="checkbox"/> <u>5938764</u>	August 1999	Klein	713/1
<input type="checkbox"/> <u>5987605</u>	November 1999	Hill et al.	713/2
<input type="checkbox"/> <u>6073206</u>	June 2000	Piwonka	711/103

<input type="checkbox"/>	<u>6141767</u>	October 2000	Hu et al.	714/1
<input type="checkbox"/>	<u>6167532</u>	December 2000	Wisecup	714/23

ART-UNIT: 2187

PRIMARY-EXAMINER: Gossage; Glenn

ATTY-AGENT-FIRM: Sierra Patent Group, Ltd.

ABSTRACT:

A system and method for providing dynamic configuration Read Only Memory (ROM) using double image buffers for use with serial bus devices. The dynamic configuration ROM may be updated while linked to the serial bus and with little or no risk of publishing inconsistent configuration ROM information to other nodes on the bus. The dynamic configuration ROM comprises first and second configuration ROM images, one set to active, the other set to update. The dynamic configuration ROM publishes the configuration entries from the active configuration ROM image. Modifications to the configuration ROM are stored in a database. The update configuration ROM image is constructed from entries made to the database. After the construction of the update configuration ROM image is completed, the dynamic configuration ROM switches the states of the ROM images and transmits a serial bus reset signal. Subsequently, the configuration entries from the newly active configuration ROM images are then published to the other nodes on the bus.

10 Claims, 6 Drawing figures

